Project 1: Logic Gates and Combinational Chips

Student name:

Self-declaration: Check the chips in the column "Done?" that you have designed and are willing to present in class.

Grading method: The implementation of some chips is simpler than for others; the different weights assigned to the groups of chips below reflect this variation. If *all* chips in a group pass *all* tests specified in the supplied test script, it receives its allotted points.

You do not get points for individual chips in a group.

Chip	*Done?*	*Working?*	*Points*	*Comments*
Not			/ 1	
And				
Or				
Mux				
DMux				
And16			/ 1	
Or8Way				
Mux16				
Mux4Way16				
DMux4Way				
HalfAdder			/ 1	
FullAdder				
Add16				
Inc16				
ALU			/ 2	
Total			**/ 5**	