IE1204 Digital Design Answer Form 2022-2023

Full Name			Personal Number							Program
#	Answer with	Answer							Points	
1	Decimal number									
2	8 bit two's complement binary number		I			I			1	
3	8 bit two's complement binary number									
4	Circuit number(s)	ļ	<u> </u>		ļ	<u> </u>	ļ	ļ	<u> </u>	
ľ										
5	Boolean expression, Y =									
6	Boolean expression, Y =									
	1.41.11									
7	MUX connections, Boolean expression or Gate									
	Row CD = 00									1
	Row CD = 01									
	KOM CD - OT									-
	Row CD = 10									
										1
	Row CD = 11									
8	Timing diagram									
	0 ms 5 ms 10 ms 15 ms 20 ms 25 ms	30 ms		35 ms		40 ms		45 ms	-	
	CLK			П						
	Q									
9	ming diagram									
	0 ms 5 ms 10 ms 15 ms 20 ms 25 ms	30 ms		35 ms		40 ms		45 ms		
	CLK	Ш	_							
	Q									
40		1								
10	Propagation delay t _{pd} ≤								ps	-
	Contamination delay t _{cd} >								ps	
11	Next state $Q_3Q_2Q_1Q_0 =$									
12	Boolean expression or Gate, Y =									
13	16 bit two's complement hexadecimal P									
13	16 bit two's complement hexadecimal Product A x B									
14	8 bit two's complement hexadecimal	Q				R				
- '	Quotient (A / B) and Remainder									
15	8 result bits (S ₇ S ₆ S ₅ S ₄ S ₃ S ₂ S ₁ S ₀)									
	Shift register contents, 8 bits									
•			Examiner sign							