

Lab 3, Analysis and Design of Discrete Controllers

IE1304 Control Theory

1 Goal

The goal is to learn how to design a discrete controller, using pole placement. You will design the controller and analyze its characteristics (settling time, stability, overshoot, steady-state error).

2 The Process

The process to control is exactly the same as in lab 2 and is depicted in fig 1. It consists of two water tanks, the lower tank is filled from the upper tank, which in turn is filled by a pump. There is an outlet from the lower tank and, to introduce a disturbance, also the upper tank has an outlet. The measured value is the level of the lower tank.

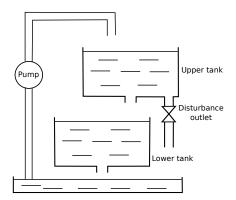


Figure 1: The controlled process



3 Preparation Tasks, to be solved BEFORE the lab

Task 1, Reading

- Read Chapter 19.1-19.5 in the course text book, and understand how a controller can be designed with pole placement.
- Read Section 4 in this tutorial and try to understand what to do during the lab.

Task 2, Controller Design

a) Pole Placement

There is no need to measure time constants and amplification again since the process to control is identical to the process in lab 2. Instead, use the values you measured in lab 2 to create a second order model, $G_P = \frac{K_P}{(1+T_1s)(1+T_2s)}$. If you are unsure of your measured values you can set $K_P = 0.9$, $T_1 = 6s$ and $T_2 = 21s$. Set the sampling time to 10% of the process' fastest (smallest) time constant and discretize the transfer function G_P . For discretization, use Matlab and the command c2d.

Recall the effects of different pole locations mentioned at lecture 11. Use the controller design described at lecture 11 and on page 351 in the text book which gives the transfer function from reference value to measured value, $H(z) = \frac{K_r B(z)}{A(z)C(z) + B(z)D(z)}$. Place the poles in the area showed in the figure on page 355 in the course text book.

Place the poles in the area showed in the figure on page 355 in the course text book. The process is quite slow, therefore we must place the poles close to the unit circle or we will get too high values for control input, u(k). You could for example place the poles in $0.8 \pm j0.3$ or $0.7 \pm j0.2$. Since both A(z) and B(z) is of second order the pole placement method described in the text book will tell that three poles are needed, you can place a third pole in origo.

To design the controller, you shall calculate K_r , C(z) and D(z). It is very important that you finish this calculation before the lab since it is quite time consuming.

b) Evaluation

Use Simulink to simulate the step response of the whole feedback loop (controller and process together). Remember that the control input is the pump voltage. A resonable value for the step is from 5V to 6V. Are the system properties (settling time, stability, overshoot and steady-state error) acceptable? Remember to check the step response also of the control input, u(k). At the lab, the highest possible value of u(k) is about 8.5V.

c) Integrating Controller

Improve your controller by introducing an integrator as described in section 19.4 in the text book. Again use Simulink to simulate the step response of y(k) and u(k). Also again evaluate the system's settling time, stability, overshoot and steady-state error.



Task 3, Controller Implementation

The controller will be the same PLC system that was used during lab 2. Implement the controller you designed in the previous task in a program in ISaGRAF. You can reuse the pidcontr program from lab 2 and change the sampling time in steps GS1, GT9, GT10 and the control law in step GS10.

4 Lab Tasks, to be solved at the lab

Task 1, Analysis of Non-Integrating Controller

- a) Set up the lab equipment the same way you did in lab 2, see the tutorial for lab 2 for details.
- b) Download your controller program to Smart I/O and set the reference value to for example 5000. Let the system stabilize and then make a small change in reference value, for example to 5200. Plot the step response of the system using Fluke View and measure settling time, stability, overshoot and steady-state error of the process when using your controller.
- c) Measure settling time, stability, overshoot and steady-state error of the process when using your controller and introducing a disturbance.
- d) Does the system behave as expected? If it does not, why do you think it behaves differently?
- e) If your system has a steady-state error, try to elliminate it by adjusting the controller gain, K_r .

Task 2, Analysis of Integrating Controller

Evaluate the integrating controller the same way you evaluated the non-integrating controller in Task 1.