



# ID2218 Design of Fault-tolerant Systems 7.5 credits

## Design of Fault-tolerant Systems

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Course syllabus for ID2218 valid from Autumn 08

This is a translation of the Swedish, legally binding, course syllabus.

**Grading scale:** A, B, C, D, E, FX, F

**Education cycle:** Second cycle

**Main field of study:** Electrical Engineering

### Intended learning outcomes

The aims of this course are:

- to create understanding of the fundamental concepts of fault-tolerance
- to learn basic techniques for achieving fault-tolerance in electronic, communication and software systems
- to develop skills in modeling and evaluating fault-tolerant architectures in terms of reliability, availability and safety
- to gain knowledge in sources of faults and means for their prevention and forecasting
- to understand merits and limitations of fault-tolerant design

### Course main content

Fault tolerance is the ability of a system to continue performing its intended function despite of faults. In a broad sense, fault tolerance is associated with reliability, with successful operation, and with the absence of breakdowns.

The ultimate goal of fault tolerance is the development of a dependable system. As computer systems become relied upon by society more and more, dependability of these systems becomes a critical issue. In airplanes, chemical plants, heart pace-makers or other safety critical applications, a system failure can cost people's lives or environmental disaster.

There are various approaches to achieve fault-tolerance. Common to all these approaches is a certain amount of redundancy. This can be a replicated hardware component, an additional check bit attached to a string of digital data, or a few lines of program code verifying the correctness of the program's results. In this course, we are going to study hardware as well and software fault tolerance. The rapid development of real-time computing applications that started around the mid-1990s, especially the demand for software-embedded intelligent devices, made software fault tolerance a pressing issue.

The following is a tentative list of topics to be covered:

- Introduction
- Definition of fault tolerance
- Redundancy
- Applications of fault-tolerance
- Fundamentals of dependability
- Attributes: reliability, availability, safety
- Impairments: faults, errors and failures
- Means: fault prevention, removal and forecasting
- Dependability evaluation

- Common measures: failures rate, mean time to failure, mean time to repair, etc.
- Reliability block diagrams
- Markov processes
- Hardware redundancy
- Redundancy schemes
- Evaluation and comparison
- Applications
- Information redundancy
- Codes: linear, Hamming, cyclic, unordered, arithmetic, etc.
- Encoding and decoding techniques
- Applications
- Time redundancy
- Software fault tolerance
- Specific features
- Software fault tolerance techniques: N-version programming, recovery blocks, self-checking software, etc.

## Language of instruction

Language of instruction is specified in the course offering information in the course and programme directory.

## Eligibility

## Literature

Course notes E. Dubrova, "Fault-Tolerant Design: An Introduction" (draft, will be distributed in the class).

## Examination

- ANN1 - Assignment, 1.5 credits, grading scale: A, B, C, D, E, FX, F
- TEN1 - Examination, 1.5 credits, grading scale: A, B, C, D, E, FX, F
- TEN2 - Examination, 4.5 credits, grading scale: A, B, C, D, E, FX, F

## Requirements for final grade

The final grade is based on five homework assignments (20%), a midterm exam (20%) and a final exam (60%). For PhD students, an additional task will be to read and present a paper approved by the instructor (20 min talk).