CH101V RISC-V based Embedded Systems 2.0 credits

RISC-V baserade inbyggda system

This is a translation of the Swedish, legally binding, course syllabus.

Establishment

Course syllabus for CH101V valid from Autumn 2023

Grading scale

P, F

Education cycle

First cycle

Main field of study

Technology

Specific prerequisites

At least 5 credits in Digital Electronics, Micro Computer System Design, Embedded Systems, or hardware dependent C programming. Alternative, one year of professional experience in developing embedded systems. English B/6.

Language of instruction

The language of instruction is specified in the course offering information in the course catalogue.

Intended learning outcomes

Course contents

Examination

- LAB1 - Computer exercises, 1.0 credits, grading scale: P, F
- TEN1 - Examination, 1.0 credits, grading scale: P, F

Based on recommendation from KTH's coordinator for disabilities, the examiner will decide how to adapt an examination for students with documented disability.

The examiner may apply another examination format when re-examining individual students.

If the course is discontinued, students may request to be examined during the following two academic years.

Ethical approach

- All members of a group are responsible for the group's work.
- In any assessment, every student shall honestly disclose any help received and sources used.
- In an oral assessment, every student shall be able to present and answer questions about the entire assignment and solution.