



IS1200 Computer Hardware Engineering 7.5 credits

Datorteknik, grundkurs

This is a translation of the Swedish, legally binding, course syllabus.

Establishment

Course syllabus for IS1200 valid from Spring 2019

Grading scale

A, B, C, D, E, FX, F

Education cycle

First cycle

Main field of study

Technology

Specific prerequisites

- Registered on course IE1204 or IE1205
- Registered on course ID1018 or ID1004

Language of instruction

The language of instruction is specified in the course offering information in the course catalogue.

Intended learning outcomes

After the course, the student will be able to:

- Implement low-level programs in the C programming language and in an assembly language.
- Analyze processor microarchitectures, with and without a pipeline.
- Explain the principles of memory hierarchies, including cache structures.
- Implement low-level programs with input-output, timers, and interrupts.
- Explain the principles of multiprocessor computers and how they can be programmed.
- Explain and describe technical solutions, both orally and in writing.

Course contents

The course gives fundamental knowledge of computer organization, both from a hardware and software perspective. The course is organized in six modules that include, among other things, the following concepts and terms:

1. C and Assembly Programming: pointers, functions, stack, assembly language, machine language, instruction coding, and processor registers.
2. Input/Output Systems: timers, interrupts, and memory mapped input/output.
3. Digital Design: truth tables, gates, Boolean algebra, multiplexer, decoder, adder, combinational logic, sequential logic, and registers.
4. Processor Design: arithmetic logic unit, datapath, control unit, and pipeline.
5. Memory Hierarchy: instruction cache, data cache, and virtual memory.
6. Parallel Processors and Programs: Amdahl's law, various kinds of parallelism, and multicore.

Note that module 3 is recommended prerequisites for the course and will not be covered in lectures or laboratory exercises. However, the course material for module 3 will be available on the course web page because the knowledge of this module is prerequisite for module 4.

Course literature

Selected chapters from the books:

- David Money Harris and Sarah L. Harris. Digital Design and Computer Architecture, Second Edition, Morgan Kaufmann, 2012.
- David A. Patterson and John L. Hennessy. Computer Organization and Design – the Hardware/Software Interface, Fifth Edition, Morgan Kaufmann, 2013.

The course literature also includes material from lectures, laboratory exercises, and exercise sessions.

Examination

- TEN1 - Examination, 3.0 credits, grading scale: A, B, C, D, E, FX, F
- LAB1 - Laboratory Work, 4.5 credits, grading scale: P, F

Based on recommendation from KTH's coordinator for disabilities, the examiner will decide how to adapt an examination for students with documented disability.

The examiner may apply another examination format when re-examining individual students.

If the course is discontinued, students may request to be examined during the following two academic years.

Ethical approach

- All members of a group are responsible for the group's work.
- In any assessment, every student shall honestly disclose any help received and sources used.
- In an oral assessment, every student shall be able to present and answer questions about the entire assignment and solution.