



# Programme syllabus

Master's Programme, Design and Implementation of ICT Products and Systems, 120 credits

Masterprogram, konstruktion och realisering av IT-produkter och -system

*120.0 credits*

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*Valid for students admitted to the education from autumn 07 (HT - Autumn term; VT - Spring term).*

This is a translation of the Swedish, legally binding, programme syllabus.

## Programme objectives

**Knowledge and understanding**

**Skills and abilities**

**Ability to make judgements and adopt a standpoint**

## Extent and content of the programme

## Eligibility and selection

## Implementation of the education

### Courses

The programme is course-based. Lists of courses are included in [appendix 1](#).

### Grading system

Courses in the first and the second cycle are graded on a scale from A to F. A-E are passing grades, A is the highest grade. The grades pass (P) and fail (F) are used for courses under certain circumstances.

[Appendix 1 - Course list](#)

[Appendix 2 - Programme syllabus descriptions](#)





# Appendix 1: Course list

Master's Programme, Design and Implementation of ICT Products and Systems, 120 credits (TDIPM), Programme syllabus for studies starting in autumn 2007

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## General courses

### Year 2

#### Mandatory courses (7.5 Credits)

| Course code            | Course name   | Credits | Edu. level   |
|------------------------|---|---------|--------------|
| <a href="#">IC2003</a> | <a href="#">Research Methodology and Scientific Communication</a> | 7.5 hp  | Second cycle |

#### Conditionally elective courses

| Course code            | Course name  | Credits | Edu. level   |
|------------------------|--|---------|--------------|
| <a href="#">ID2009</a> | <a href="#">Artificial Intelligence: Principles and Techniques</a> | 7.5 hp  | Second cycle |
| <a href="#">ID2202</a> | <a href="#">Compilers and Execution Environments</a>               | 7.5 hp  | Second cycle |
| <a href="#">IL2200</a> | <a href="#">ASIC-design Methodology with High-level Languages</a>  | 7.5 hp  | Second cycle |
| <a href="#">IS2451</a> | <a href="#">Embedded Systems Engineering</a>                       | 7.5 hp  | Second cycle |
| <a href="#">IS2500</a> | <a href="#">RFID Systems</a>                                       | 7.5 hp  | Second cycle |



## Appendix 2: Specialisations

Master's Programme, Design and Implementation of ICT Products and Systems, 120 credits (TDIPM), Programme syllabus for studies starting in autumn 2007

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This programme has no specialisations.