Programme syllabus

Master's Programme, Design and Implementation of ICT Products and Systems, 120 credits
Masterprogram, konstruktion och realisering av IT-produkter och -system
120.0 credits

Valid for students admitted to the education from autumn 07 (HT - Autumn term; VT - Spring term).

This is a translation of the Swedish, legally binding, programme syllabus.

Programme objectives

Knowledge and understanding

Skills and abilities

Ability to make judgements and adopt a standpoint

Extent and content of the programme

Eligibility and selection

Implementation of the education

Courses

The programme is course-based. Lists of courses are included in appendix 1.

Grading system

Courses in the first and the second cycle are graded on a scale from A to F. A-E are passing grades, A is the highest grade. The grades pass (P) and fail (F) are used for courses under certain circumstances.

Appendix 1 - Course list
Appendix 2 - Programme syllabus descriptions
Appendix 1: Course list

Master's Programme, Design and Implementation of ICT Products and Systems, 120 credits (TDIPM), Programme syllabus for studies starting in autumn 2007

General courses

Year 2

Mandatory courses (7.5 Credits)

<table>
<thead>
<tr>
<th>Course code</th>
<th>Course name</th>
<th>Credits</th>
<th>Edu. level</th>
</tr>
</thead>
<tbody>
<tr>
<td>IC2003</td>
<td>Research Methodology and Scientific Communication</td>
<td>7.5 hp</td>
<td>Second cycle</td>
</tr>
</tbody>
</table>

Conditionally elective courses

<table>
<thead>
<tr>
<th>Course code</th>
<th>Course name</th>
<th>Credits</th>
<th>Edu. level</th>
</tr>
</thead>
<tbody>
<tr>
<td>ID2009</td>
<td>Artificial Intelligence: Principles and Techniques</td>
<td>7.5 hp</td>
<td>Second cycle</td>
</tr>
<tr>
<td>ID2202</td>
<td>Compilers and Execution Environments</td>
<td>7.5 hp</td>
<td>Second cycle</td>
</tr>
<tr>
<td>IL2200</td>
<td>ASIC-design Methodology with High-level Languages</td>
<td>7.5 hp</td>
<td>Second cycle</td>
</tr>
<tr>
<td>IS2451</td>
<td>Embedded Systems Engineering</td>
<td>7.5 hp</td>
<td>Second cycle</td>
</tr>
<tr>
<td>IS2500</td>
<td>RFID Systems</td>
<td>7.5 hp</td>
<td>Second cycle</td>
</tr>
</tbody>
</table>
Appendix 2: Specialisations

Master's Programme, Design and Implementation of ICT Products and Systems, 120 credits (TDIPM), Programme syllabus for studies starting in autumn 2007

This programme has no specialisations.