



Programme syllabus

[An accessible version of the syllabus can be found in the Course and programme directory.](#)

Master's Programme, System-on-Chip Design 120 credits

Masterprogram, systemkonstruktion på kisel

Valid for students admitted to the education from autumn 07 (HT - Autumn term; VT - Spring term).

This is a translation of the Swedish, legally binding, programme syllabus.

Programme objectives

Knowledge and understanding

Skills and abilities

Ability to make judgements and adopt a standpoint

Extent and content of the programme

Eligibility and selection

Implementation of the education

Courses

The programme is course-based. Lists of courses are included in appendix 1.

Grading system

Courses in the first and the second cycle are graded on a scale from A to F. A-E are passing grades, A is the highest grade. The grades pass (P) and fail (F) are used for courses under certain circumstances.

Appendix 1 - Course list

Appendix 2 - Programme syllabus descriptions



Appendix 1: Course list

Master's Programme, System-on-Chip Design (TSKKM)

General courses

Year 1

Mandatory courses (30.0 Credits)

Code	Name	Credits	Edu. level
IL2200	ASIC-design Methodology with High-level Languages	7.5 hp	Second cycle
IL2206	Embedded Systems	7.5 hp	Second cycle
IL2207	System-On-Chip Architectures	7.5 hp	Second cycle
IL2217	Digital Design with HDL	7.5 hp	Second cycle

Optional courses

Code	Name	Credits	Edu. level
IL2205	Applied Signal Processing	7.5 hp	Second cycle

Conditionally elective courses

Code	Name	Credits	Edu. level
ID2218	Design of Fault-tolerant Systems	7.5 hp	Second cycle
II2202	Research Methodology and Scientific Writing	7.5 hp	Second cycle
IL2202	System Modelling	7.5 hp	Second cycle
IL2204	DSP-Construction with HDL	7.5 hp	Second cycle
IL2208	Electronic System Packaging	7.5 hp	Second cycle
IL2209	Advanced Logic Design	7.5 hp	Second cycle
IL2211	System-on-Chip Applications	7.5 hp	Second cycle
IL2212	Embedded Software	7.5 hp	Second cycle
IL2219	Radio Electronics	7.5 hp	Second cycle
IL2220	Low Power Analogue and Mixed Signal ICs	7.5 hp	Second cycle
IS1202	Computer Systems Architecture	7.5 hp	First cycle

Year 2

Mandatory courses (22.5 Credits)

Code	Name	Credits	Edu. level
AK2036	Theory and Methodology of Science with Applications (Natural and Technological Science)	7.5 hp	Second cycle
EH2720	Management of Projects	7.5 hp	Second cycle
IL2213	Design Project Course I	7.5 hp	Second cycle

Conditionally elective courses

Code	Name	Credits	Edu. level
IC2003	Research Methodology and Scientific Communication	7.5 hp	Second cycle
ID2201	Distributed Systems, Basic Course	7.5 hp	Second cycle
ID2202	Compilers and Execution Environments	7.5 hp	Second cycle
ID2207	Modern Methods in Software Engineering	7.5 hp	Second cycle
II2300	Product Realization Processes I	7.5 hp	Second cycle
II2500	Product Realization Processes II	7.5 hp	Second cycle
IL2221	Advanced Topics in Mixed Mode Design	7.5 hp	Second cycle
IL2452	System Design Languages	7.5 hp	Second cycle
IV2024	Principles of Computer Security	7.5 hp	Second cycle



Appendix 2: Specialisations

Master's Programme, System-on-Chip Design (TSKKM)

This programme has no specialisations.